PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

TEST CIRCUIT IN CLOCK SYNCHRONOUS SEMICONDUCTOR MEMORY DEVICE

In re Application of

Docket No.: 394-1597

Seiji SAWADA et al.

Serial No. 08/246,582

Filed: May 19, 1994



Group Art Unit: 2511

Examiner: A. Zarabian

FECENCED

AUG 28 1995

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THE COMMISSIONER OF PATENTS AND TRADEMARKS Washington, D. C. 20231

Dear Sir:

For:

Transmitted herewith is an Amendment in the above identified application.

[X] No additional fee is required.

Also attached:

The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	36	36	0	x \$22 =	0
Independent Claims	11	11	0	x \$76 =	0
		If multiple claims newly presented, add \$240.00			
		Fee for extension of time			0
TOTAL FEE DUE				0	

- [] Please charge my Deposit Account No. <u>12-2237</u> in the amount of \$0. An additional copy of this transmittal sheet is submitted herewith.
- [X] The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 12-2237, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

LOWE, PRICE, LEBLANC & BECKER

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Date: August 23, 1995